10/042,971

230 moves the block itself into Processor B FIFO 286 or processor C FIFO [[288]] 287 instead of the pointer or moves the block to a different buffer (not shown) that may be shared or dedicated to a processor and inserts into FIFO 286 or [[288]] 287 a pointer to the block in this buffer. In the embodiment in which a buffer is not dedicated to the processor, processor B FIFO 286 and processor C FIFO [[288]] 287 are arranged as conventional double buffers with a head and a tail pointer (in a reserved location of each respective FIFO 286, [[288]] 287 not used to write pointers or blocks as shown in t he Figure) pointing to the head and tail of the FIFO and processor A 230 moves either the appropriate pointer or the block to the tail of the FIFO 286, [[288]] 287 and updates the tail to the next 15 available location in the FIFO. If a buffer is dedicated to each processor is used, head and tail pointers are used to point to the head and tail of the buffer itself, with the buffer acting as the FIFO in a double buffer arrangement as described above.

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Please amend the paragraph beginning on page 21, line 4 as follows: